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APPLICANTS Shinji Tojo, Higashimurayama, JAPAN; Shinya Kanamitsu, Mitaka, JAPAN; Seiichi Ichihara, Hino, JAPAN;					
** CONTINUING DATA ***** This application is a CON of 10/242,720 09/13/2002 PAT 6,699,737					
** FOREIGN APPLICATIONS ***** JAPAN 2001-311540 10/09/2001					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/14/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature <i>QW</i> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 1
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TITLE Method of manufacturing a semiconductor device					
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